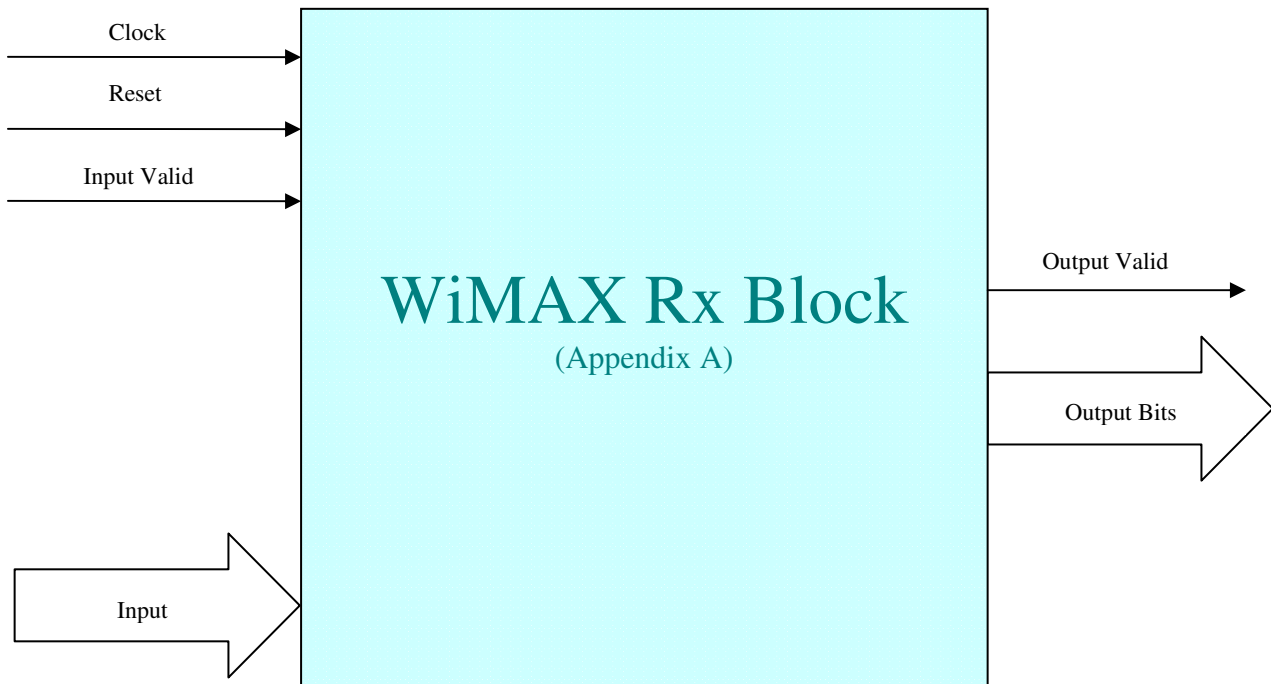


WiMAX Receiver Core



Features

- Patented OFDM Synchronization Technology.
- Optimized design allows customers to target cost efficient FPGAs.
- Can be tailored to customer needs
- Fully synchronous design using only one clock
- Area/Power efficient architecture

Deliverables

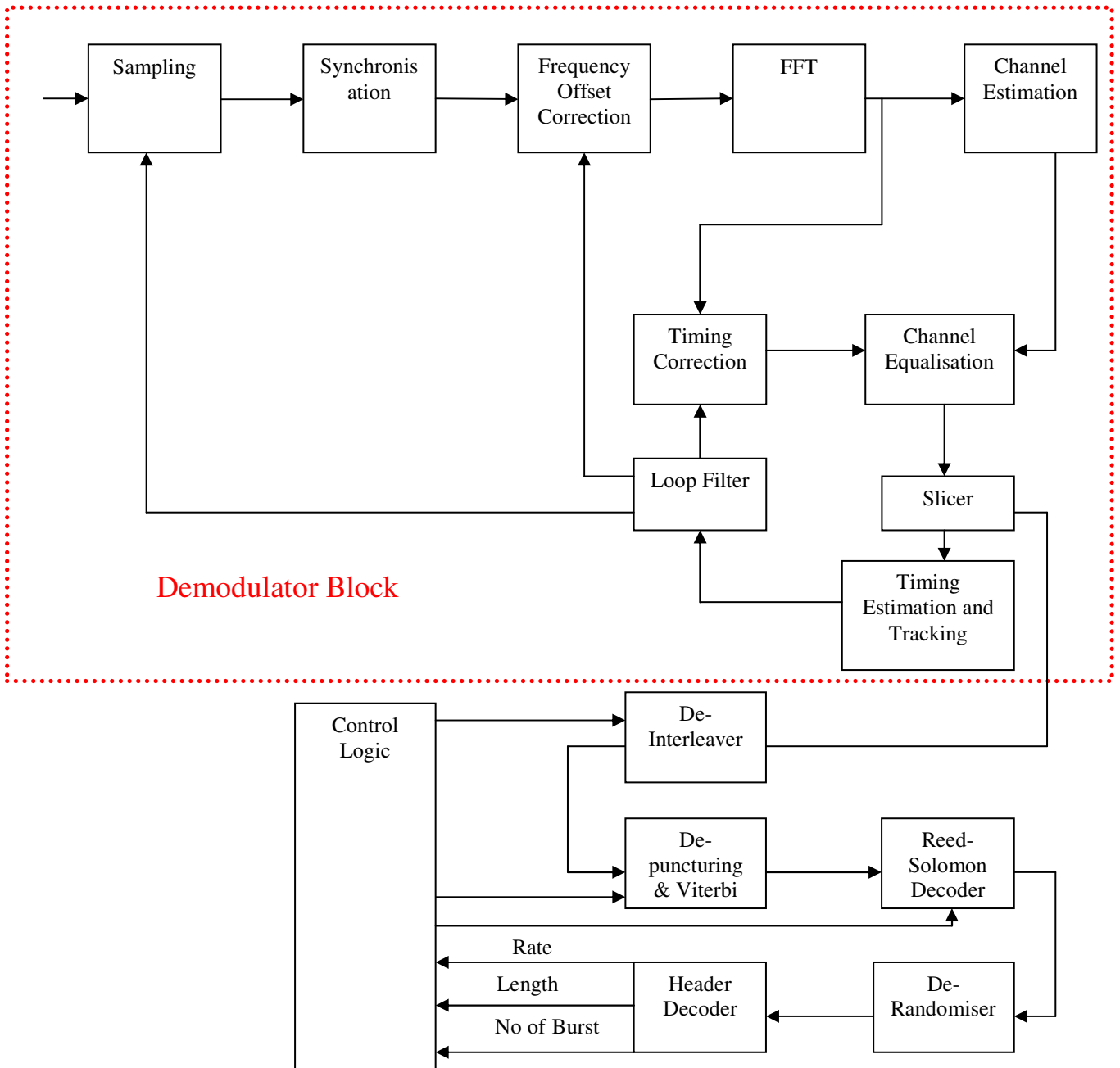
- Netlist or synthesizable RTL source code in VHDL
- Comprehensive verification test bench and vectors in VHDL
- Integration documentation and user guide

Overview

This decoder is written in VHDL, capable of being used on any FPGA/ASIC architecture.

Appendix A

IEEE802.16d Receiver Block Diagram



Specifications subject to change without notice. Information furnished by IPCoreWorx is believed to be accurate and reliable. However, no responsibility is assumed by IPCoreWorx for its use. All company and product names are trademarks or registered trademarks of their respective owners. All rights reserved. © 2014 IPCoreWorx.